6677 0" SM8260

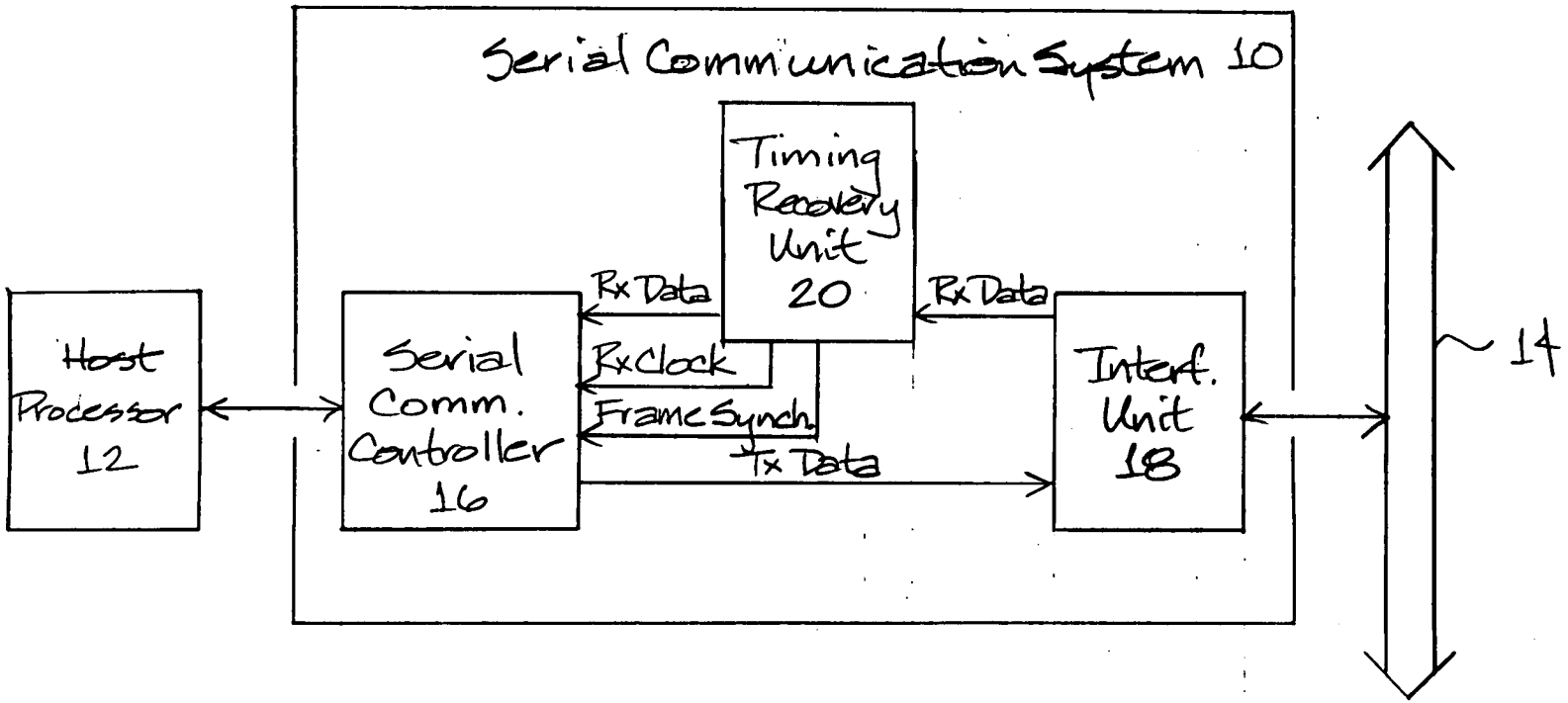

Fig. 1

66FFTD' SM3260

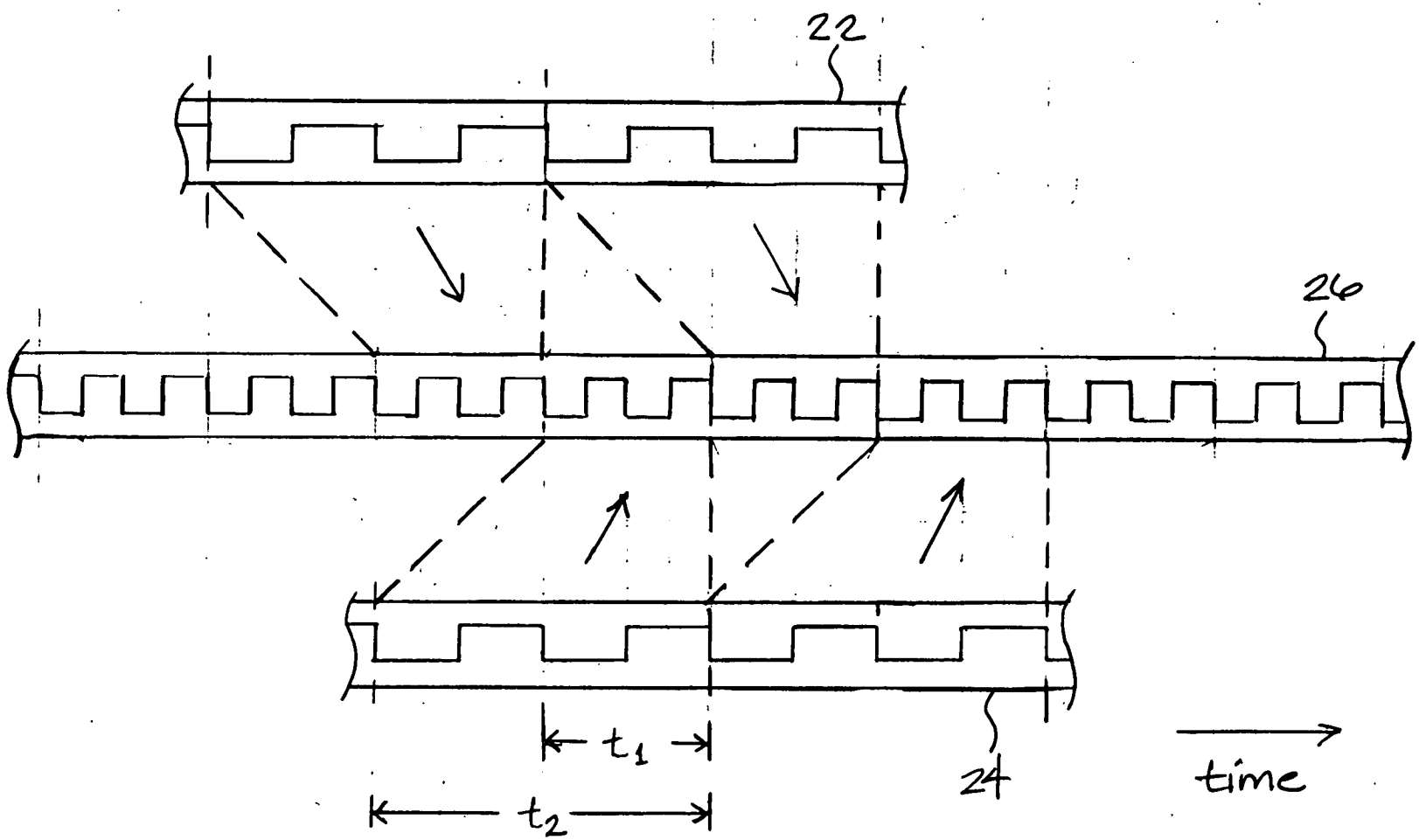

Fig. 2

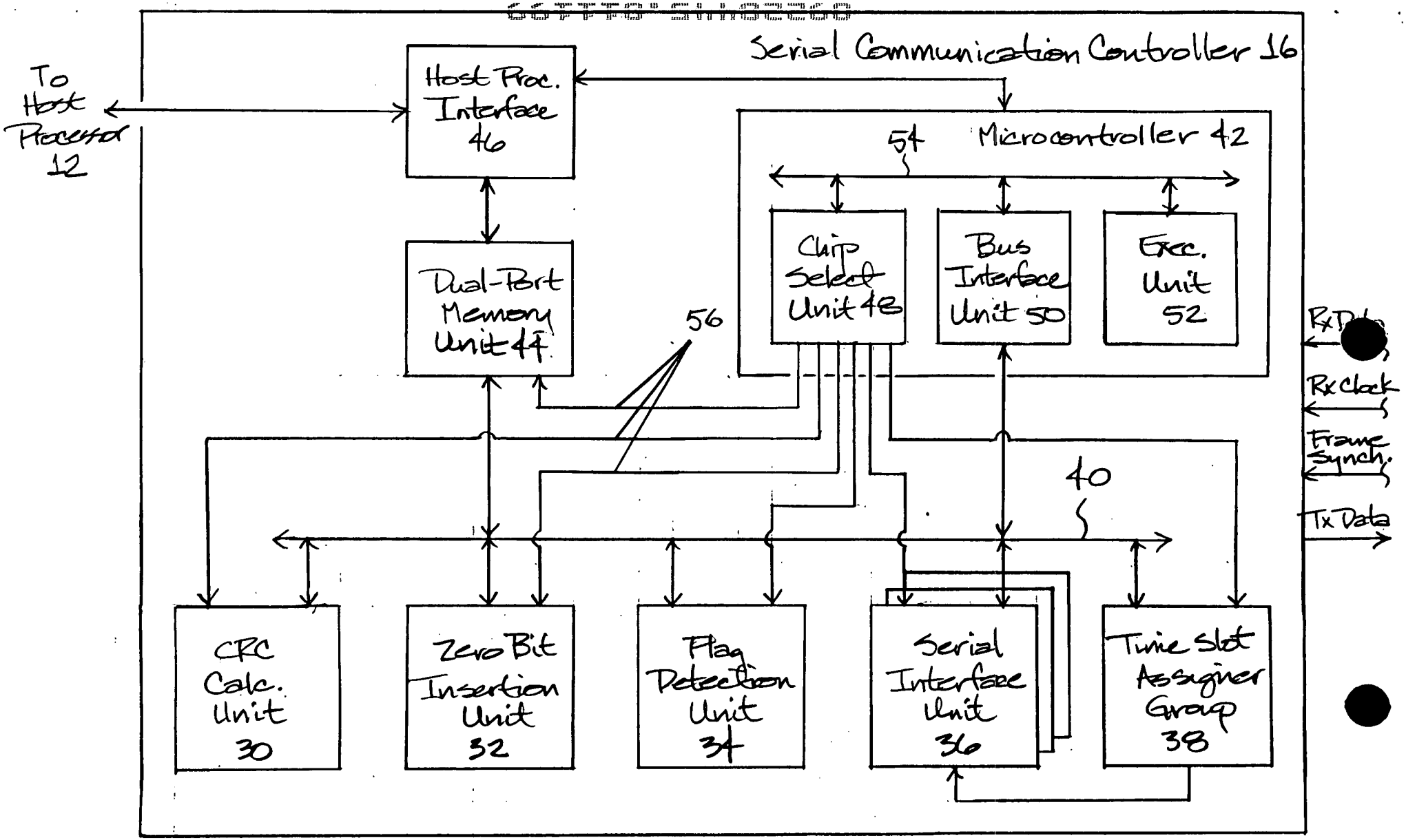
Serial Communication Controller 16
To Host Processor 12
Host Proc. Interface 46
Microcontroller 42
54
Chip Select Unit 48
Bus Interface Unit 50
Exec. Unit 52
Dual-Port Memory Unit 44
56
40
RxData
Rx Clock
Frame Synch.
Tx Data
CRC Calc. Unit 30
Zero Bit Insertion Unit 32
Flag Detection Unit 34
Serial Interface Unit 36
Time Slot Assigner Group 38

Fig. 3

Opening Flag Field	Address Field	Control Field	Information Field	Frame Check Sequencing Field	Closing Flag Field
01111110	8 bits	8 bits	8*n bits	16/32 bits	01111110

Fig. 4

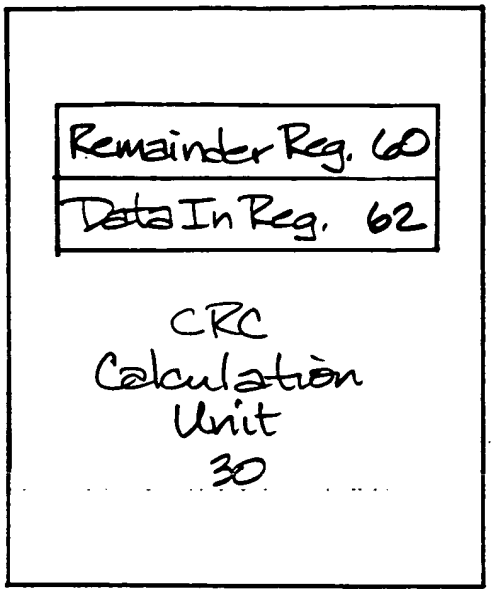
Remainder Reg. 60
Data In Reg. 62
CRC
Calculation
Unit
30

Fig. 5

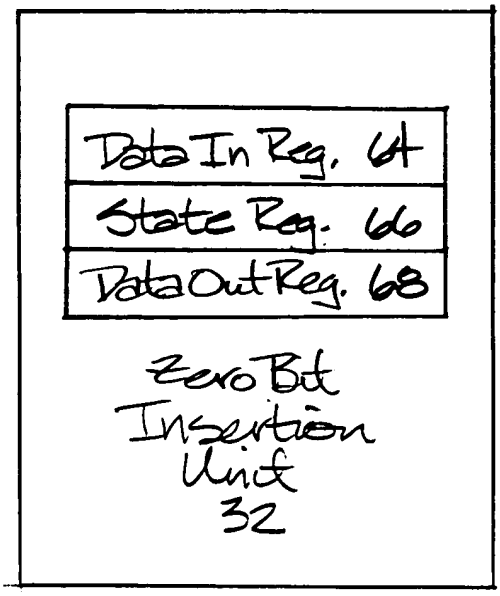
Data In Reg. 64
State Reg. 66
Data Out Reg. 68
Zero Bit
Insertion
Unit
32

Fig. 6

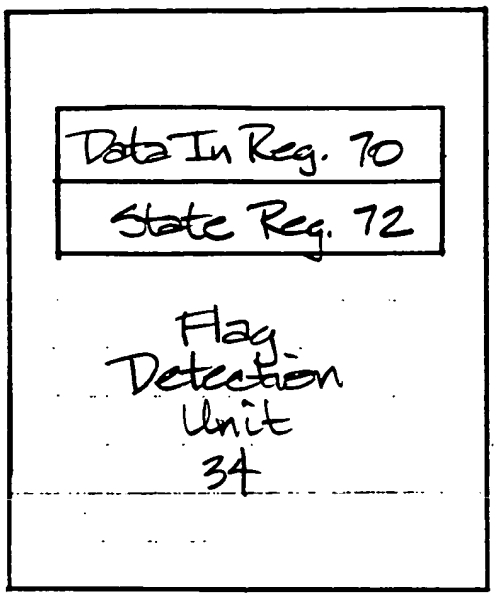
Data In Reg. 70
State Reg. 72
Flag Detection Unit 34

Fig. 7

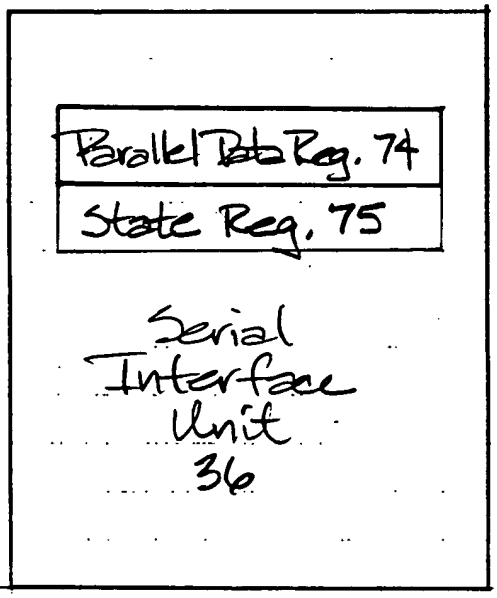
Parallel Data Reg. 74
State Reg. 75
Serial Interface Unit 36

Fig. 8

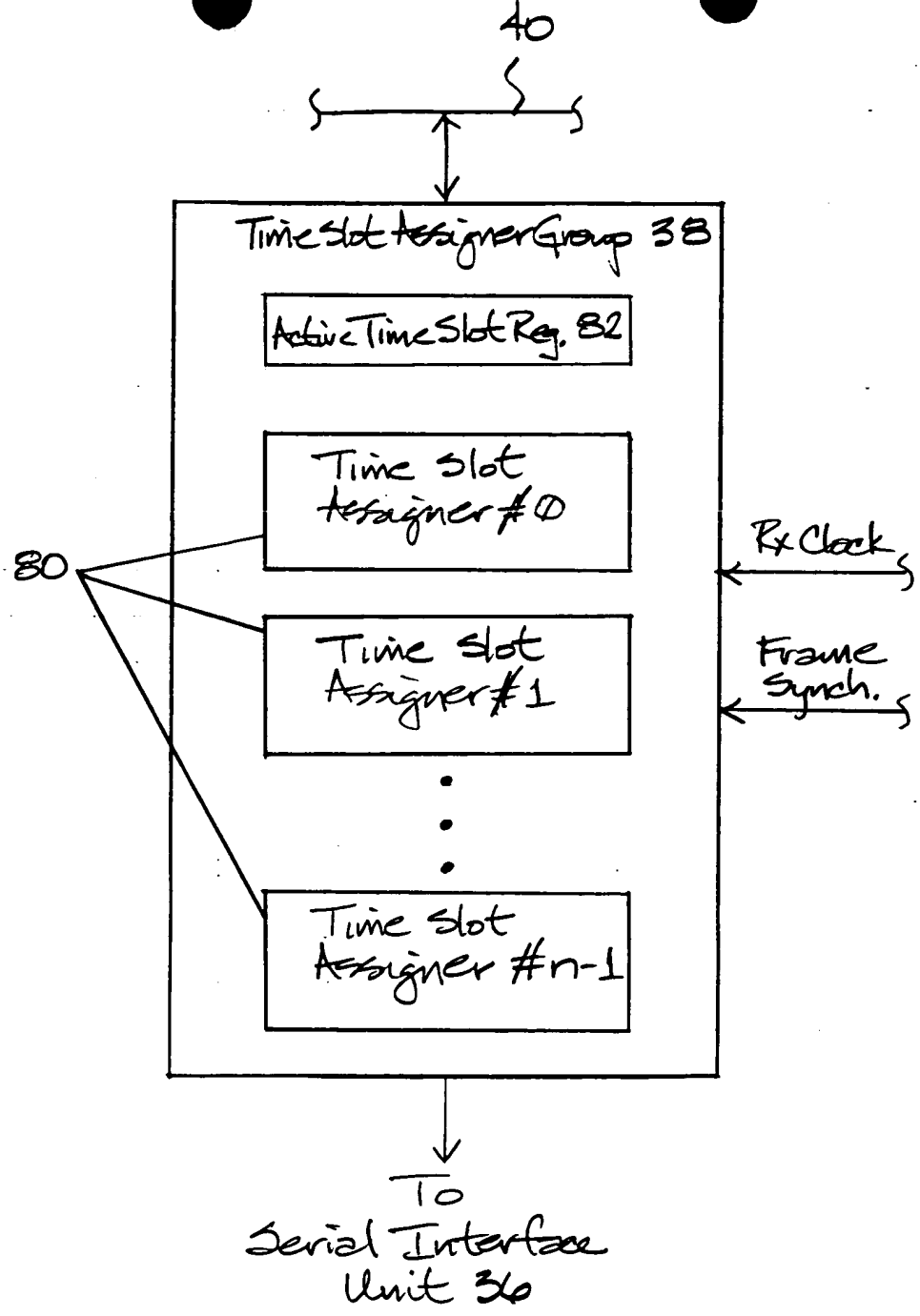
40
Time Slot Assigner Group 38
Active Time Slot Reg. 82
Time Slot Assigner #0
Time Slot Assigner #1
Time Slot Assigner #n-1
80
Rx Clock
Frame Synch.
To Serial Interface Unit 36

Fig. 9

66TTO" SM8260

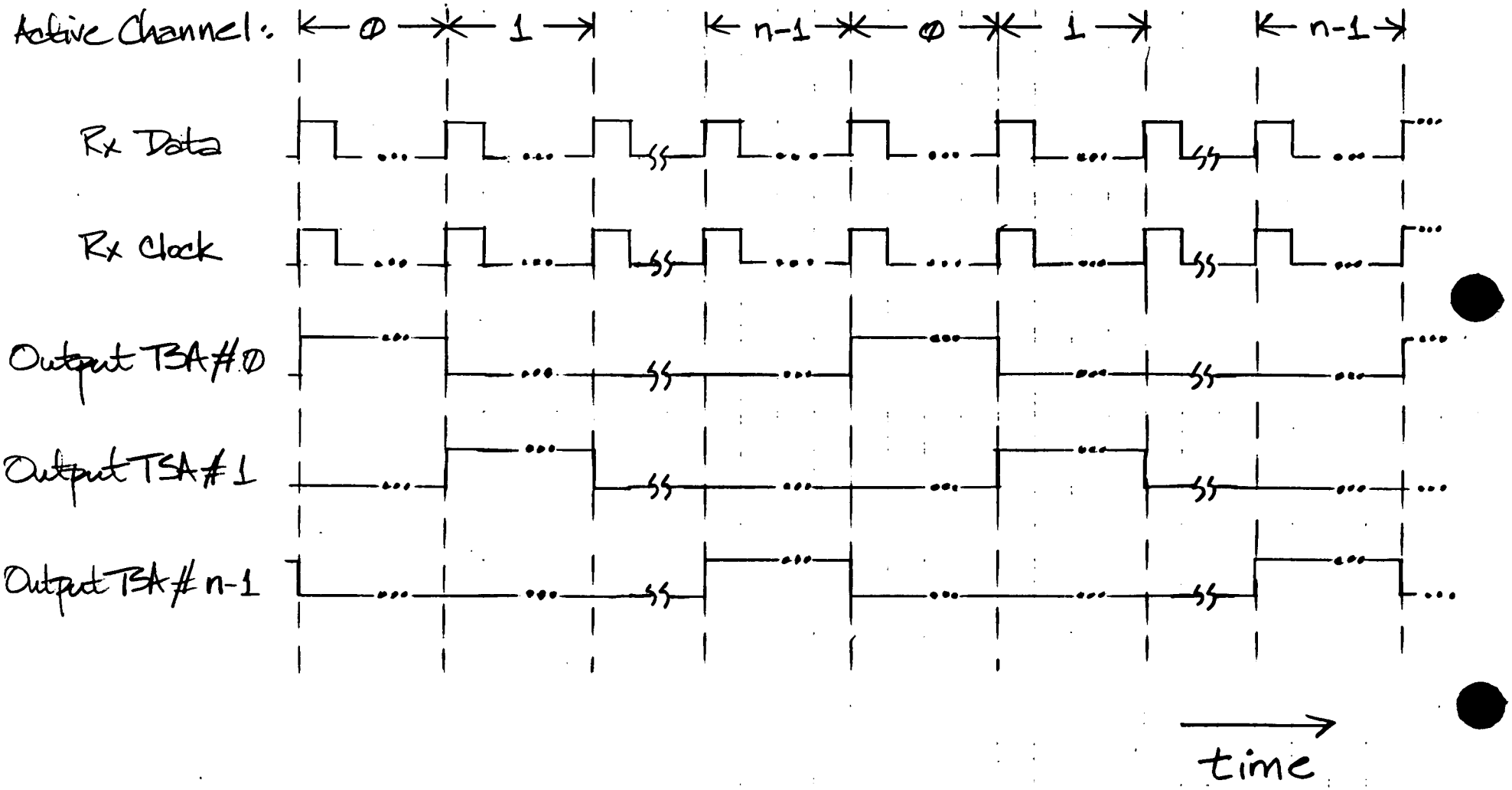

Fig. 10

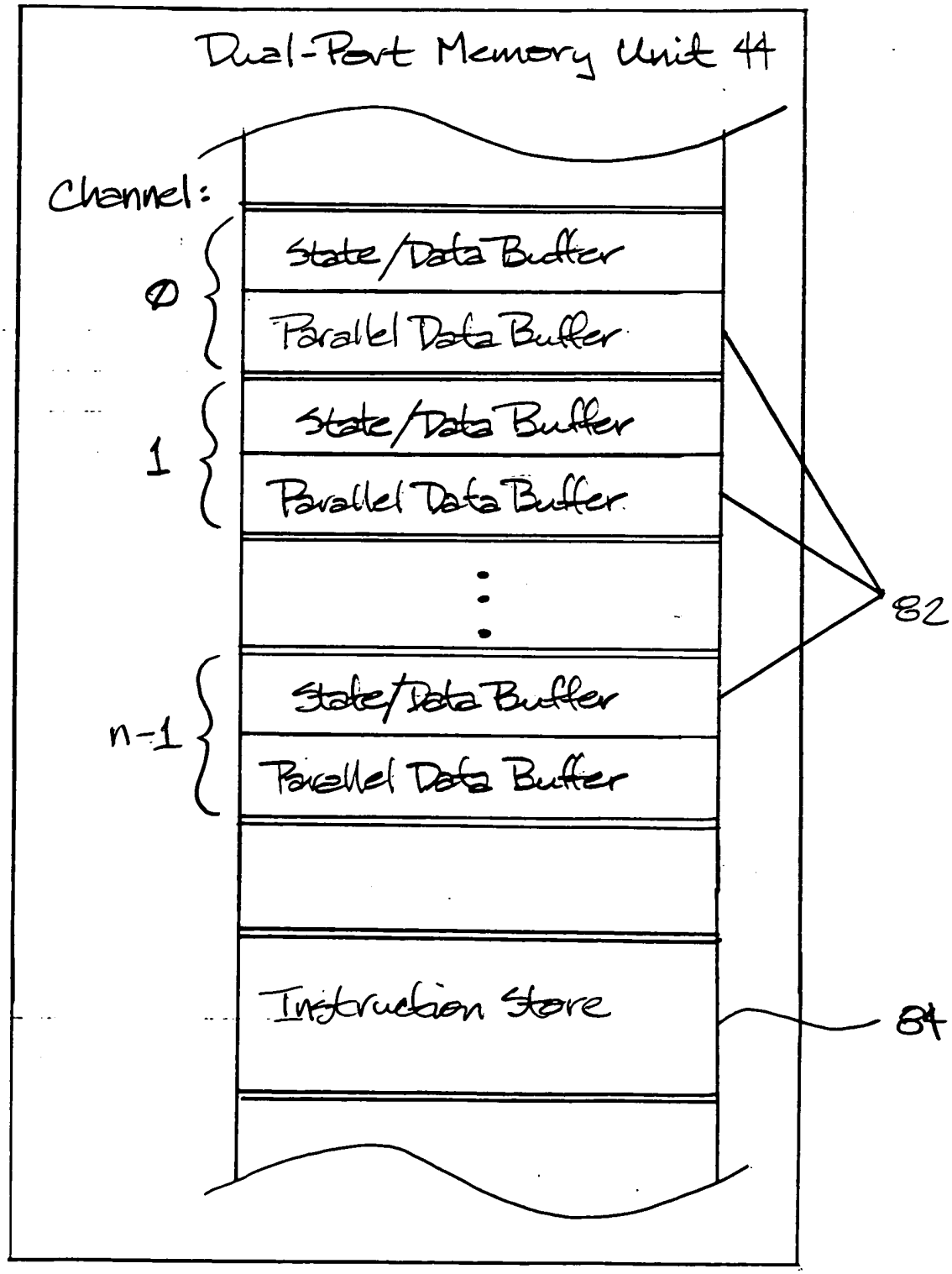
Dual-Port Memory Unit 44
Channel:
0
State/Data Buffer
Parallel Data Buffer
1
State/Data Buffer
Parallel Data Buffer
n-1
State/Data Buffer
Parallel Data Buffer
Instruction Store
82
84

Fig. 11